

DUAL EMITTER TRANSISTOR WITH ESD PROTECTION

Abstract of the Disclosure

The invention provides a method and circuit design for providing ESD protection. Specifically, a dual emitter transistor is used to provide bipolar operation and ESD protection. The first emitter provides bipolar operation under normal operating conditions. The second emitter provides an alternative current path for ESD protection.

Figures

Figure 1: A line graph showing the relationship between the number of hours spent studying and the score on a test. The x-axis represents the number of hours (0 to 10), and the y-axis represents the score (0 to 100). The data points are as follows:

Hours	Score
0	50
1	55
2	60
3	65
4	70
5	75
6	80
7	85
8	90
9	95
10	100

The graph shows a positive linear relationship, indicating that as the number of hours spent studying increases, the score on the test also increases.